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APPLICANTS Patrick Lysaght, Los Gatos, CA; Tim Tuan, San Jose, CA; Goran Bilski, Molndal, SWEDEN; <i>None NL</i>					
** CONTINUING DATA ***** <i>None NL</i>					
** FOREIGN APPLICATIONS *****					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 01/24/2004					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met <i>None</i> Allowance <i>NL</i> Verified and Acknowledged <i>None</i> Examiner's Signature Initials		STATE OR COUNTRY CA	SHEETS DRAWING 5	TOTAL CLAIMS 20/8	INDEPENDENT CLAIMS 4
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TITLE Method and apparatus for power optimization during an integrated circuit design process					
FILING FEE RECEIVED 856	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		